

CLAIMS

1. A mirror detection signal generator, characterized by the fact that the mirror detection signal generator, which generates a mirror detection signal from an RF signal corresponding the reflected light from a recording medium, comprises the following parts:

a first peak-hold circuit that holds the bottom level of said RF signal at a first attenuation rate and outputs a bottom-hold signal;

a second peak-hold circuit that holds the top level of said RF signal at a second attenuation rate and outputs a first envelope signal;

a third peak-hold circuit that holds the bottom level of said RF signal at a third attenuation rate and outputs a second envelope signal;

a first reference signal generator that outputs the first reference signal generated on the basis of said bottom-hold signal and said first envelope signal;

and a first comparator that compares said first reference signal with said second envelope signal and generates said mirror detection signal.

2. The mirror detection signal generator described in Claim 1, characterized by the fact that

said first reference signal generator comprises a first voltage divider that divides the voltage of said bottom-hold signal and said first envelope signal.

3. The mirror detection signal generator described in Claim 2, characterized by the fact that

said first reference signal generator comprises a first amplifier that amplifies said divided voltage at an amplification rate corresponding to the type of recording medium.

4. The mirror detection signal generator described in Claim 3, characterized by the fact that

said first reference signal generator comprises an offset circuit that adds a prescribed offset voltage to the output signal of said first amplifier.

5. The mirror detection signal generator described in Claim 1, 2, 3 or 4, characterized by the fact that it comprises

a filter that performs a prescribed signal processing for said second envelope signal, and
a second amplifier that amplifies said second envelope signal at an amplification rate
corresponding to the type of recording medium.

6. The mirror detection signal generator described in Claim 1, 2, 3, 4 or 5, characterized
by the fact that it comprises

a fourth peak-hold circuit that holds the top level of said RF signal at a fourth attenuation
rate and outputs a top-hold signal;

a second reference signal generator that outputs the second reference signal generated on
the basis of said top-hold signal and said bottom-hold signal;

and a second comparator that compares said second reference signal and said first
envelope signal and generates a defect detection signal.

7. The mirror detection signal generator described in Claim 6, characterized by the fact
that

said second reference signal generator comprises a second voltage divider that divides the
voltage of said top-hold signal and said bottom-hold signal.

8. The mirror detection signal generator described in Claim 7, characterized by the fact
that

said second reference signal generator comprises a third amplifier that amplifies the
divided voltage output from said second voltage divider at an amplification rate corresponding to
the type of recording medium.

9. The mirror detection signal generator described in Claim 7 or 8, characterized by the
fact that

it comprises a fourth amplifier that amplifies said first envelope signal at an amplification
rate corresponding to the type of recording medium.

10. The mirror detection signal generator described in Claim 6, 7, 8 or 9, characterized by
the fact that

when said defect detection signal is output, said first attenuation rate is increased.